

Filed

THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Juan G. Revilla, et al. Art Unit: 2185 Examiner : Unknown

Serial No.: 10/040,920

: December 28, 2001

: METHOD FOR EXTENDING THE LOCAL MEMORY ADDRESS SPACE Title

OF A PROCESSOR

Commissioner for Patents Washington, D.C. 20231

PRELIMINARY AMENDMENT

Prior to examination, please amend the application as follows:

In the specification:

RECEIVED

Replace the paragraph number 0016 with the following Technology Center 2100 rewritten paragraph:

-- The execution unit 120 may request access to non-This may occur due to mistakes in the program existent memory. and in instances when the program wanders outside of the enabled L1 SRAM memory address space. If the access is to non-existent L1 SRAM memory, the local memory controller 135 may trigger an illegal-access violation exception (block 216). The execution flow may then be interrupted in order for the processor 102 to handle the exception. --

CERTIFICATE OF MAILING BY FIRST CLASS MAIL

05/20/2002 BNGUYEN1 00000128 10040920

01 FC:103 02 FC:102

72.00 OP 168.00 OP I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, Washington, D.C. 20231.

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Susan Regan

Typed or Printed Name of Person Signing Certificate

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COPY OF PAPERS ORIGINALLY FILED

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TRANSMITTAL LETTER AND PETITION FOR AUTOMATIC EXTENSION

Correspondence relating to this application is enclosed. The required fees are computed below. Please apply any charges not covered, or any credits, to Deposit Account No. 06-1050.

Total Claims \$72 2 Independent \$168

A check for \$240 is attached.

Respectfully submitted,

MAY 2 3 2002

Technology Center 2100

\$240

Fish & Richardson P.C. PTO Customer No. 20985

4350 La Jolla Village Drive, Suite 500

San Diego, California 92122 Telephone: (858) 678-5070

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